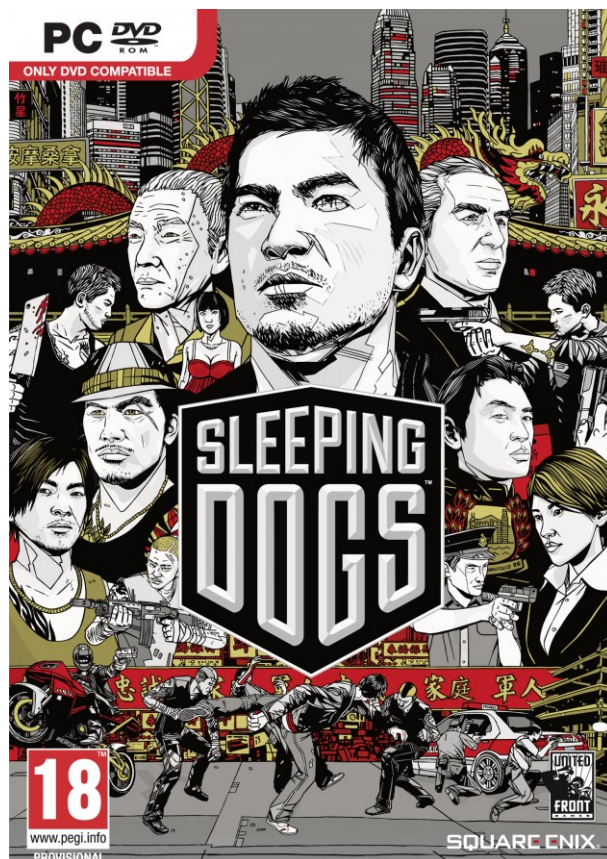


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. The Sempron, named after its design, has a dual-core version of the same speed as the Phenom X4 but with only one core working at a time; while this is the better choice in terms of power consumption and heat, it is slower than the Phenom in terms of performance. It was therefore replaced by the Phenom II X4 955e as AMD's next-generation dual-core CPU. Brazos architecture The Brazos platform is a 100-nm microarchitecture designed by Bull in 2004. It was intended to succeed the previous top-end Athlon 64-bit architecture. It contains eight DIMM sockets, more cache (a 4 MB L2 cache shared between the two cores and a 2 MB shared L1 instruction cache), a PCI Express 2.0 slot and other improvements. It also has a low TDP of 35–40 W and 64 W low-power versions for mobile devices. Brazos is based on the early 2000s Pentium 4 architecture, but with a few variations. It was intended to be the basis for AMD's next-generation Athlon 64-bit microarchitecture, which would have been based on the 250-nm process. However, due to the 2006 AMD acquisition by its rival, Intel, the Athlon 64 project was scrapped and the first AMD chips were based on the 100-nm process. Brazos chips are released in the form of Dual-core and Quad-core processors. Brazos processors The following table lists the parts with code names.

1 GB = 1066 MHz = 1 GHz = 1 GHz = 999 MHz = 974 MHz = 955 MHz;

1.2 GB = 1.33 GHz = 1.333 GHz = 1.334 GHz = 1134 MHz = 1098 MHz = 1090 MHz;

1.3 GB = 1.43 GHz = 1.433 GHz = 1.434 GHz = 1184 MHz = 1096 MHz = 1092 MHz; 1.5 GB = 1.66 GHz = 1.666 GHz = 1.82157476af

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